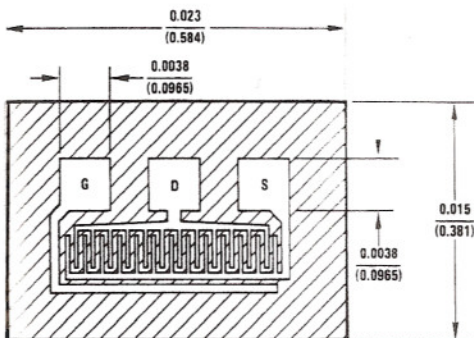




# Sierra Components, Inc.

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 Phone: 775.783.4940 Fax: 775.783.4947

Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Topside Metal: Aluminum**  
**Backside Metal: Gold**  
**Backside Potential:**  
**Bond Pad Size: .004" min.**  
**Mask Ref:**

**APPROVED BY: J Kidd**

**DIE SIZE : .015" X .023"**

**DATE: 11/28/07**

**MFG: Fairchild**

**THICKNESS: .012"**

**Part # J309**